

Electronics Analog and Digital(Study App)

1. INTRODUCTION:

Learn Analog & Digital Electronics 10+ 2, C-2 with scientific and smart way. Study material in this app is prepared by experience , qualified electronics engineer. Though this app is specially made for 10+2 bifocal Electronics C-2 subject students, it is helpful to diploma , degree students , learners , hobbyists. App Include unique Images, Videos of Practical. Maximum information about the topic is conveyed through the use of colorful diagrams to make them efficient for learning and LMR(Last minute revision). Mnemonics techniques are Incorporated at many places to make smart study .

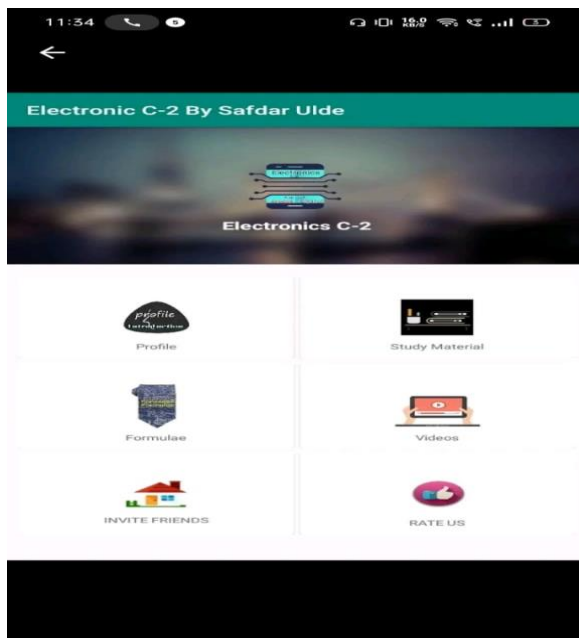
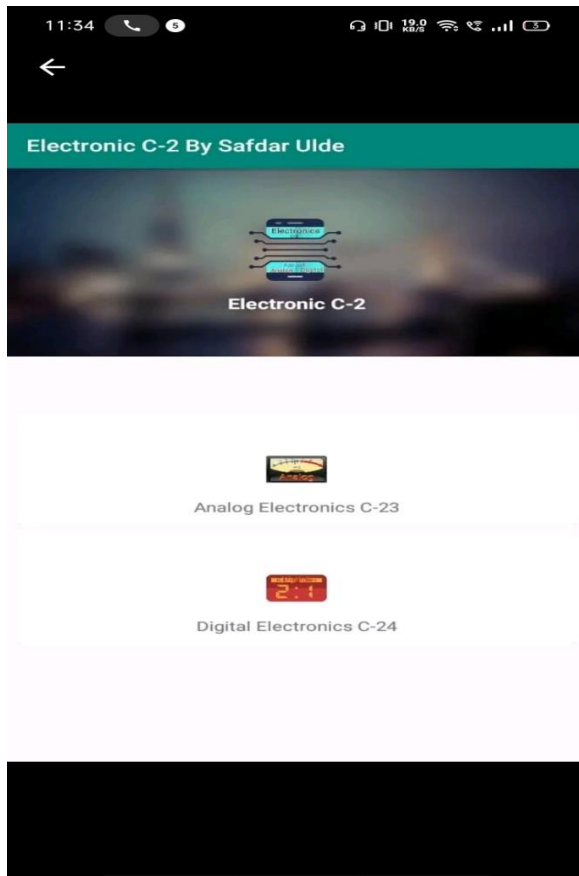
2. Features:

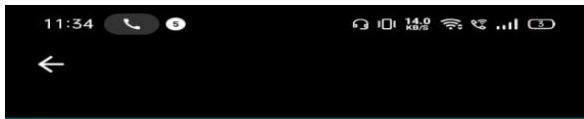
- Colourful diagrams with max topic info.
- Mnemonics where ever possible.
- Practical videos.
- Systematics Scientific study material.
- Images/Diagrams magnification is possible.

3. Link:

<https://play.google.com/store/apps/details?id=com.alfaizaid.ulde>

4. User Interface:

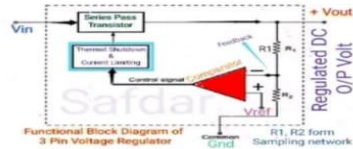




Regulator

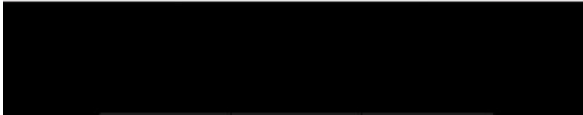
Block diagram of Basic Three Terminal IC Regulator

The figure shows the functional block diagram of basic three terminal IC regulator.



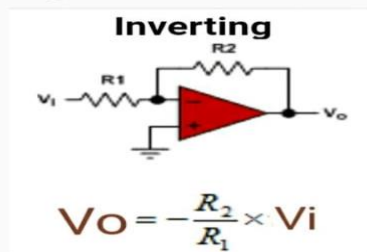
This is basically a series voltage regulator circuit. A part of output voltage is taken with the help of potential divider formed by R1 and R2. This is compared with reference voltage, Vref internally generated with the help of zener diode.

After comparison, a control signal is generated which is applied through protective circuit to the series pass transistor working as control element. This element works as a variable resistance. The control signal adjusts the control element in such a way that output voltage remains constant. Thermal shutdown means that the chip will automatically turn itself off if the internal temperature exceeds, typically, 175°C. The current limiting circuit will protect the chip from excessive load current.

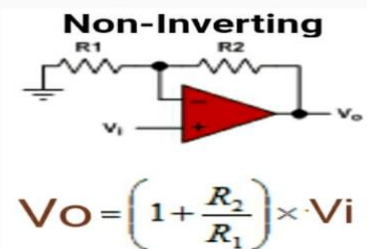


formula

1. Inverting



2. Non-Inverting



3. Inverting Adder

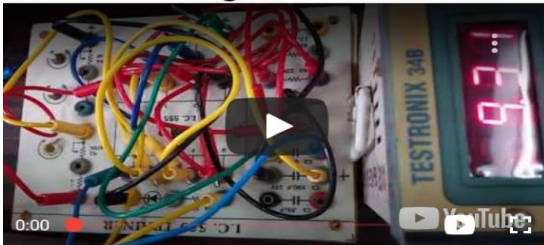
Inverting Adder



1. De Morgan's Theorems.



2. Timer Using IC 555 on Trainer.



3. Construction of basic and derived gates using NAND and NOR gates.



5. Achievement.

- Above 100 Students have downloaded this app
- Positive response on app
- Students are studying from this app.